TOSHIBA CMOS Digital Integrated Circuit Silicon Monolithic

# TC74AC153P,TC74AC153F,TC74AC153FN

### **Dual 4-Channel Multiplexer**

The TC74AC153 is an advanced high speed CMOS DUAL 4-CHANNEL MULTIPLEXER fabricated with silicon gate and double-layer metal wiring  $C^2MOS$  technology.

It achieves the high speed operation similar to equivalent Bipolar Schottky TTL while maintaining the CMOS low power dissipation.

Each of these data (1C0-1C3, 2C0-2C3) is selected by the two address inputs A and B.

Separate strobe inputs  $(1\overline{G}\;,\;\;2\overline{G}\;)$  are provided for each of the two four-line sections.

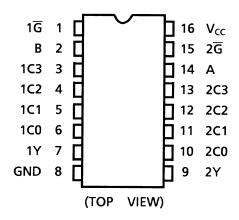
The strobe input can be used to inhibit the data output; the output is fixed in low level unconditionally.

All inputs are equipped with protection circuits against static discharge or transient excess voltage.

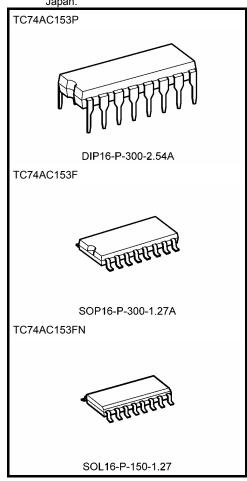
#### **Features**

- High speed:  $t_{pd} = 3.9 \text{ ns (typ.)}$  at  $V_{CC} = 5 \text{ V}$
- Low power dissipation:  $I_{CC} = 8 \mu A \text{ (max)}$  at  $T_{a} = 25 \text{°C}$
- High noise immunity: V<sub>NIH</sub> = V<sub>NIL</sub> = 28% V<sub>CC</sub> (min)
- Symmetrical output impedance:  $|I_{OH}| = I_{OL} = 24$  mA (min) Capability of driving 50  $\Omega$  transmission lines.
- Balanced propagation delays:  $t_{pLH} \simeq t_{pHL}$
- Wide operating voltage range: VCC (opr) = 2 to 5.5 V
- Pin and function compatible with 74F153

#### Pin Assignment



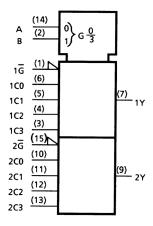
Note: xxxFN (JEDEC SOP) is not available in Japan.



Weight

DIP16-P-300-2.54A : 1.00 g (typ.) SOP16-P-300-1.27A : 0.18 g (typ.) SOL16-P-150-1.27 : 0.13 g (typ.)

# **IEC Logic Symbol**



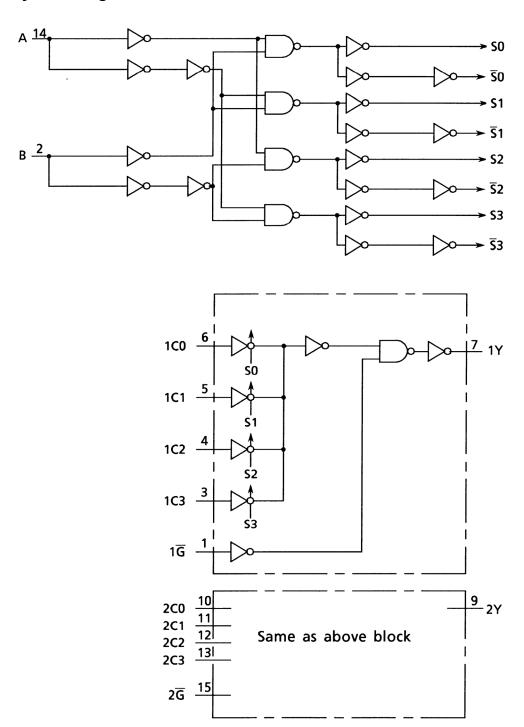
# **Truth Table**

Select Inputs			Data	Inputs	Strobe	Output		
В	Α	C0	C1	C2	C3	IG	Y	
Х	Х	Х	Х	Х	Х	Н	L	
L	L	L	Х	Х	Х	L	L	
L	L	Н	Х	Х	Х	L	Н	
L	Н	Х	L	Х	Х	L	L	
L	Н	Х	Н	Х	Х	L	Н	
Н	L	Х	Х	L	Х	L	L	
Н	L	Х	Х	Н	Х	L	Н	
Н	Н	Х	Х	Х	L	L	L	
Н	Н	Х	Х	Х	Н	L	Н	

X: Don't care

2

# **System Diagram**





### **Absolute Maximum Ratings (Note 1)**

Characteristics	Symbol	Rating	Unit
Supply voltage range	V <sub>CC</sub>	-0.5 to 7.0	V
DC input voltage	V <sub>IN</sub>	-0.5 to V <sub>CC</sub> + 0.5	V
DC output voltage	V <sub>OUT</sub>	-0.5 to V <sub>CC</sub> + 0.5	V
Input diode current	Ι <sub>ΙΚ</sub>	±20	mA
Output diode current	lok	±50	mA
DC output current	lout	±50	mA
DC V <sub>CC</sub> /ground current	Icc	±100	mA
Power dissipation	P <sub>D</sub>	500 (DIP) (Note 2)/180 (SOP)	mW
Storage temperature	T <sub>stg</sub>	-65 to 150	°C

Note 1: Exceeding any of the absolute maximum ratings, even briefly, lead to deterioration in IC performance or even destruction.

Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings and the operating ranges.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/Derating Concept and Methods) and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Note 2: 500 mW in the range of Ta = -40 to 65°C. From Ta = 65 to 85°C a derating factor of -10 mW/°C should be applied up to 300 mW.

## **Operating Ranges (Note)**

Characteristics	Symbol	Rating	Unit	
Supply voltage	V <sub>CC</sub>	2.0 to 5.5	V	
Input voltage	V <sub>IN</sub>	0 to V <sub>CC</sub>	V	
Output voltage	V <sub>OUT</sub>	0 to V <sub>CC</sub>	V	
Operating temperature	T <sub>opr</sub>	-40 to 85	°C	
Input rise and fall time	dt/dV	0 to 100 ( $V_{CC} = 3.3 \pm 0.3 \text{ V}$ )	ns/V	
input rise and fail time	avav	0 to 20 (V <sub>CC</sub> = 5 $\pm$ 0.5 V)	115/ V	

Note: The operating ranges must be maintained to ensure the normal operation of the device. Unused inputs must be tied to either VCC or GND.



#### **Electrical Characteristics**

#### **DC Characteristics**

Characteristics	Symbol	Test Condition			Ta = 25°C			Ta = -40 to 85°C		Unit	
Characteristics	Symbol				V <sub>CC</sub> (V)	Min	Тур.	Max	Min	Max	Offic
					2.0	1.50	_	_	1.50	_	
High-level input voltage	$V_{IH}$	_			3.0	2.10	_	_	2.10	_	V
Ŭ				5.5	3.85	_	_	3.85			
		_			2.0	_	_	0.50	_	0.50	
Low-level input voltage	$V_{IL}$			3.0	_	_	0.90	_	0.90	V	
, and the second				5.5	_	_	1.65	_	1.65		
	V <sub>ОН</sub>				2.0	1.9	2.0	_	1.9	_	
		VIN = VIH or VIL	I <sub>OH</sub> = -50 μA		3.0	2.9	3.0	_	2.9	_	
High-level output voltage					4.5	4.4	4.5	_	4.4	_	V
			$I_{OH} = -4 \text{ mA}$		3.0	2.58	_	_	2.48	_	V
			I <sub>OH</sub> = -24 mA		4.5	3.94	_	_	3.80	_	
			$I_{OH} = -75 \text{ mA}$	(Note)	5.5	_	_	_	3.85	_	
	V <sub>OL</sub>	VIN = VIH or VIL			2.0	_	0.0	0.1	_	0.1	
			Ι <sub>ΟL</sub> = 50 μΑ		3.0	_	0.0	0.1	_	0.1	
Low-level output voltage					4.5	_	0.0	0.1	_	0.1	V
			$I_{OL} = 12 \text{ mA}$		3.0	_	_	0.36	_	0.44	·
			$I_{OL} = 24 \text{ mA}$		4.5	_	_	0.36	_	0.44	
			$I_{OL} = 75 \text{ mA}$	(Note)	5.5	_	_	_	_	1.65	
Input leakage current	I <sub>IN</sub>	V <sub>IN</sub> = V <sub>CC</sub> or GND		5.5	_	_	±0.1	_	±1.0	μΑ	
Quiescent supply current	Icc	V <sub>IN</sub> = V <sub>CC</sub> or GND		5.5	_	_	8.0	_	80.0	μΑ	

Note: This spec indicates the capability of driving 50  $\Omega$  transmission lines.

One output should be tested at a time for a 10 ms maximum duration.

## AC Characteristics (C<sub>L</sub> = 50 pF, R<sub>L</sub> = 500 $\Omega$ , input: $t_r = t_f = 3$ ns)

Characteristics	Symbol	Test Condition		Ta = 25°C			Ta = -40 to 85°C		Unit	
	.,		V <sub>CC</sub> (V)	Min	Тур.	Max	Min	Max		
Propagation delay time (Cn-Y)	t <sub>pLH</sub>	_	$3.3 \pm 0.3$ $5.0 \pm 0.5$	_ _	7.6 5.0	14.5 9.0	1.0 1.0	16.5 10.3	ns	
Propagation delay time (A, B-Y)	t <sub>pLH</sub>	_	$3.3 \pm 0.3$ $5.0 \pm 0.5$	_ _	10.5 6.6	20.5 10.5	1.0 1.0	23.4 12.0	ns	
Propagation delay time $(\overline{G} - Y)$	t <sub>pLH</sub>	_	$3.3 \pm 0.3$ $5.0 \pm 0.5$	_ _	6.8 4.4	13.3 8.0	1.0 1.0	15.2 9.1	ns	
Input capacitance	C <sub>IN</sub>	_		_	5	10	_	10	pF	
Power dissipation capacitance	C <sub>PD</sub> (Note)	_		_	54	_	_	_	pF	

Note: C<sub>PD</sub> is defined as the value of the internal equivalent capacitance which is calculated from the operating current consumption without load.

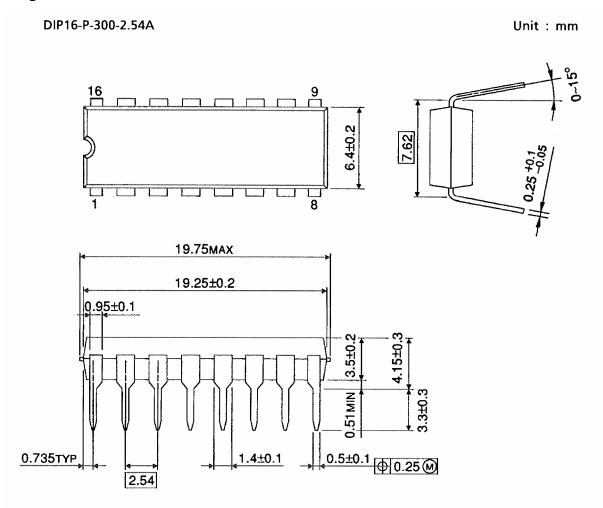
5

Average operating current can be obtained by the equation:

 $I_{CC}$  (opr) =  $C_{PD} \cdot V_{CC} \cdot f_{IN} + I_{CC}$ 

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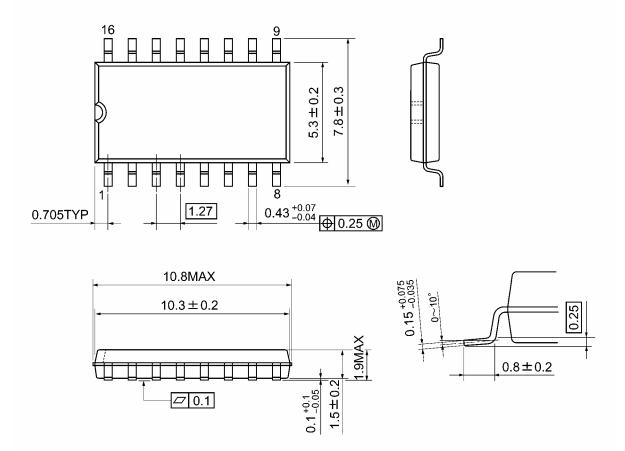
# **Package Dimensions**



Weight: 1.00 g (typ.)

# **Package Dimensions**

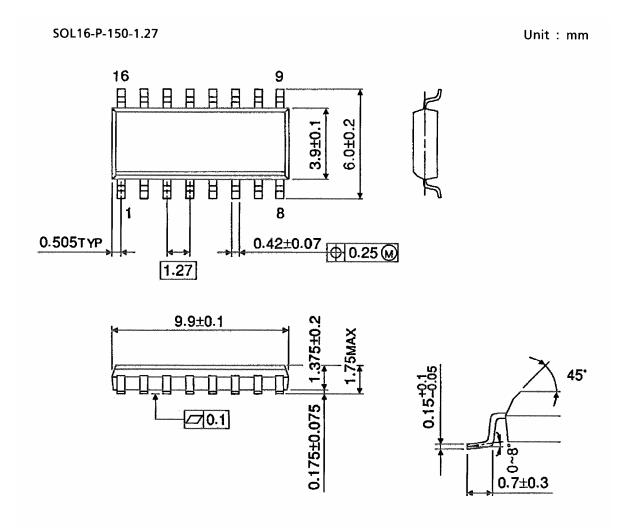
SOP16-P-300-1.27A Unit: mm



Weight: 0.18 g (typ.)



# **Package Dimensions (Note)**



8

Note: This package is not available in Japan.

Weight: 0.13 g (typ.)

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20070701-EN GENERAL

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9